

A virgola mobile  
 FLD Load Real onto ST  
 FST Store Real from ST  
 FSTP Store Real from ST and POP  
 FXCH Exchange Register with ST  
 Interi  
 FILD Load Integer onto ST  
 FIST Store Integer from ST  
 FISTP Store Integer from ST and POP  
 Decimali codificati  
 FBLD Load BCD onto ST  
 FBSTP Store BCD from ST and POP  
 Addizione FADD Addition  
 FADDP Addition and POP  
 FIADD Integer addition  
 Sottrazione FSUB Subtract  
 FSUBP Subtract and POP  
 FISUB Integer subtract  
 FSUBR Reverse subtract  
 FSUBRP Reverse subtract and POP  
 FISUBR Reverse integer subtract  
 Moltiplicazione FMUL Multiplication  
 FMULP Multiplication and POP  
 FIMUL Integer multiplication  
 Divisione FDIV Division  
 FDIVP Division and POP  
 FIDIV Integer division  
 FDIVR Reverse division  
 FDIVRP Reverse division and POP  
 FIDIVR Reverse integer division  
 Altre FPREM Partial remainder  
 FRNDINT Rounds ST to an integer  
 FABS Absolute value of ST  
 FCHS Change sign  
 FEXTRACT Extract exponent and significand of ST  
 FSCALE Scale by factor of 2  
 Trigonometriche  
 FPTAN Partial tangent  
 FPATAN Partial arctangent  
 FSIN\_ Sine  
 FCOS\_ Cosine  
 FSINCOS\_ Sine and cosine  
 Logaritmiche  
 FYL2X Compute  $y_{\log_2}(x)$   
 FYL2XP1 Compute  $y_{\log_2}(x+1)$   
 Esponenziali  
 FSQRT Square root  
 F2XMI Compute  $2^x - 1$   
 Costanti  
 FLDZ Load 0:0 on ST  
 FLD1 Load 1:0 on ST  
 FLDL2T Load  $\log_2 10$  on ST  
 FLDL2E Load  $\log_2 e$  on ST  
 FLDLG2 Load  $\log_{10} 2$  on ST  
 FLDLN2 Load  $\log_e 2$  on ST  
 Comparazione  
 FCOM Floating point compare  
 FCOMP Floating point compare and pop  
 FCOMPP Floating point compare and pop twice  
 FICOM Integer compare  
 FICOMP Integer compare and pop  
 FTST Compare ST against a 0  
 FXAM Examine ST  
 Sincronia  
 FINIT Initialize FPU  
 FNINIT Initialize FPU, no wait  
 FWAIT Wait while FPU is executing  
 FNOP No operation  
 Trasferimento stato  
 FLDCW Load control register  
 FSTCW Store control register  
 FSTSW Store status register  
 FSAVE Save FPU status  
 FRSTOR Load FPU status  
 FSTENV Save FPU environment  
 FLDENV Load FPU environment  
 Gestione stack register  
 FINCST Increment stack pointer  
 FDECSTP Decrement stack pointer  
 FFREE Free a register  
 Controllo  
 FDISI Disable interrupts  
 FENI Enable interrupts  
 FCLEX Clear error and busy flags